

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1.-18. *(canceled)*.

19. *(previously presented)* A subscriber unit for a telecommunication network, comprising:

- a connection matrix;
- a first synchronous bus;
- a second synchronous bus;
- a bus interface circuit coupled to the connection matrix, the bus interface circuit coupled to the first and second synchronous buses; and
- at least one digital subscriber card comprising:
 - a first means for concentrating Internet frames from a plurality of digital subscriber lines on $n \times 64$ kbit/s data links prior to transmitting the concentrated Internet frames through a virtual circuit to a data switch,
 - a second means for receiving an instruction supplied by a call processor to concentrate the Internet frames received from the plurality of digital subscriber lines and for controlling the first means concentrating and transmitting the Internet frames through the virtual circuit to the data switch,
 - a first carrier detect and collision resolution interface circuit coupled to the first means and coupled to a first bus, and

a second carrier detect and collision resolution interface circuit coupled to the first means and coupled to a second bus,
wherein the first synchronous bus is coupled to the first bus of the digital subscriber card, and the second synchronous bus is coupled to the second bus of the digital subscriber card.

20. *(previously presented)* A subscriber unit for a telecommunication network,
comprising:

a connection matrix;

a first synchronous bus;

a second synchronous bus;

a bus interface circuit coupled to the connection matrix, the bus interface circuit coupled to the first and second synchronous buses; and

at least one digital subscriber card comprising:

a controller for concentrating Internet frames from a plurality of digital subscriber lines on $n \times 64$ kbit/s data links prior to transmitting the concentrated Internet frames through a virtual circuit to a data switch,

a processor for receiving an instruction supplied by a call processor to concentrate the Internet frames received from the plurality of digital subscriber lines and for instructing the controller concentrating and transmitting the Internet frames through the virtual circuit to the data switch,

a first carrier detect and collision resolution interface circuit coupled to the controller and coupled to a first bus, and

a second carrier detect and collision resolution interface circuit coupled to the controller and coupled to a second bus, wherein the first synchronous bus is coupled to the first bus of the digital subscriber card, and the second synchronous bus is coupled to the second bus of the digital subscriber card.